



Prof. Vimal Bhatia

Vimal Bhatia (SM'12) is currently working as a Professor with the Indian Institute of Technology (IIT) Indore, India, and is an adjunct faculty at IIT Delhi and IIIT Delhi, India. He is also visiting faculty at UHK, Czech Republic. He received Ph.D. degree from Institute for Digital Communications with The University of Edinburgh, Edinburgh, U.K., in 2005. During Ph.D. he also received the IEE fellowship for collaborative research at the Department of Systems and Computer Engineering, Carleton University, Canada, and is Young Faculty Research Fellow from MeitY, Govt of India. He is also a recipient of Prof SVC Aiya Memorial Award (2019). He has worked with various IT companies for over 11 years both in India and the UK. He has worked with C-DOT, Hughes Software Systems, AmberFin, AudioSoft, Openwave Systems and Digital Theater Systems. He is a PI/co-PI/coordinator for external projects with funding of over USD 18 million from MeitY, DST, UKIERI, MoE, AKA, IUSSTF, TKF-FNAE and KPMG. He has more than 300 peer-reviewed publications with 70+ IEEE Journals and 22-IEEE Transactions. He has been granted 2 Indian patents. His research interests are in the broader areas of communications, non-Gaussian non-parametric signal processing, machine/deep learning with applications to communications and photonics. He is a reviewer for IEEE, Elsevier, Wiley, Springer, and IET. He is currently Senior Member of IEEE, Fellow IETE and certified SCRUM Master. He was also the General Co-Chair for IEEE ANTS 2018, and General Vice-Chair for IEEE ANTS 2017. He has served as founder head of Center for Innovation and Entrepreneurship, Associate Dean R&D and Dean, Academic Affairs. He has delivered many talks, tutorials and conducted faculty development programs for the World Bank's NPIU TEQIP-III programs. He is currently Associate Editor for IETE Technical Review, Frontiers in Communications and Networks, Frontiers in Signal Processing, and IEEE Wireless Communications Letters.